

The NDR585 is a wideband, 4 channel, 3U OpenVPX microwave tuner that provides frequency coverage from 20 MHz to 18 GHz. The NDR585's industry leading channel density minimizes system level SWaP. Each of the 4 channels provides a 1 GHz analog IF output with a 500 MHz instantaneous bandwidth. The channels can tune both independently and phase coherently and multiple NDR585 units can be synchronized for phase coherent operation. The NDR585 incorporates a super-heterodyne RF conversion architecture to minimize spurious products and yield high dynamic range performance. The fully integrated synthesizers provide fast tuning, low phase noise and the tuning flexibility to choose an alternative IF output center frequency (an IF different than 1 GHz) if desired. The NDR585 includes an internal 100 MHz OCXO and accepts a 10 MHz reference input.

The unit's form factor is single slot (1 inch pitch) 3U VPX and the total power consumption is 40 Watts (10 Watts per channel). The RF interface is provided through the VPX backplane using coaxial ports that are compliant with VITA 67.3. The backplane interface is compliant with SOSA, and VITA 46/65 OpenVPX. The NDR585 is controlled via a Gigabit Ethernet interface over the VPX backplane. The NDR585 digital architecture is based on the Xilinx 7000 series Zync SoC.

